PTO/SB/08a (08-03)

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Substitute for form 1449A/PTO Complete if Known Unknown 10/796,552 Application Number INFORMATION DISCLOSURE Filing Date March 8, 2004 STATEMENT BY APPLICANT First Named Inventor Prasenjit Biswas Art Unit Waknown 2181 (Use as many sheets as necessary) Examiner Name Unknown W. Sheet 43496.00014 of Attorney Docket Number

U.S. PATENT DOCUMENTS						
Examiner	Cite No. <sup>1</sup>	Document Number	Publication Date	Name of Patentee or Applicant of	Pages, Columns, Lines, Where Relevan	
Initials *		Number - Kind Code <sup>2</sup> (if known) MM-DD-	MM-DD-YYYY	Cited Document	Passages or Relevant Figures Appear	
MM			03-22-1966	Gloates et al.		
1		US- 4,171,537	10-16-1979	Weissberger et al.		
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		FOREIGN PA	TENT DOCU	MENTS		-
Examiner Initials*	Cite	Foreign Patent Document Public		Name of Patentee or	Pages, Columns, Lines,	
	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> ( <i>if known</i> )	Date MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	. <b>T</b> ¢
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Substitute for form 1449B/PTO Complete if Known Application Number Unknown 10/796 INFORMATION DISCLOSURE Filing Date March 8, 2004 STATEMENT BY APPLICANT First Named Inventor Prasenjit Biswas Art Unit Unknown (Use as many sheets as necessary) Examiner Name Onknown Sheet Attorney Docket Number 43496.00014

		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of	1	
Examiner Cite No.1		the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
TAW		SMITH, James E. et al.; "Implementing Precise Interrupts in Pipelined Processors"; IEEE Transactions on Computers; May 1988; pages 562-573; Vol. 37, No. 5.		
		HASEGAWA, Atsushi et al.; "SH3: High Code Density, Low Power"; IEEE Micro; December 1995; pages 11-19.		
		TURLEY, Jim; "Hitachi Adds FP, DSP Units to SuperHChips - Two New Additions Aimed at Boosting Lead in Consumer Electronics"; Microprocessor Report; December 4, 1995; pages 10-11.		
		JOHNSON, Mike, "Superscalar Microprocessor Design"; Prentice Hall; 1991; 7 pages.		
		MARGULIS, Neal; "i860 Microprocessor Architecture"; Osborne McGraw-Hill; 1990; pages 27, 37-41, 45-53, 80-94 and 205-210.		
TNW		iAPX 86/88, 186/188 User's Manual Hardware Reference; Intel; Order No. 210912-001; 3 pages, including page 3-8 (8087 Numeric Processor Extension).		
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